



Docket No.: 061352-0119

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Tohru SAITOH, et al.

Application No.: 10/564,085

Filed: January 11, 2006

For: HETEROJUNCTION BIPOLAR TRANSISTOR AND METHOD FOR
MANUFACTURING SAME

: Customer Number: 20277
:
: Confirmation Number: Not Yet Assigned
:
: Group Art Unit: Not Yet Assigned
:
: Examiner: Not yet assigned
:

INFORMATION DISCLOSURE STATEMENT

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

In accordance with the provisions of 37 C.F.R. 1.56, 1.97 and 1.98, the attention of the Patent and Trademark Office is hereby directed to the document listed on the attached form PTO-1449. It is respectfully requested that the document be expressly considered during the prosecution of this application, and that the document be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

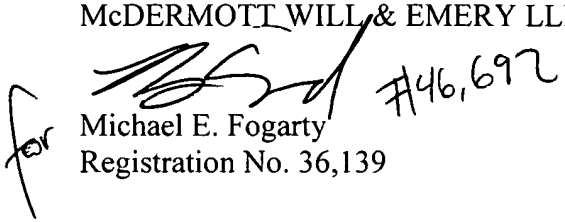
This Information Disclosure Statement is being filed within three months of the U.S. filing date OR before the mailing date of a first Office Action on the merits. No certification or fee is required.

10/564,085

Please charge any shortage in fees due in connection with the filing of this paper, including extension of time fees, to Deposit Account 500417 and please credit any excess fees to such deposit account.

Respectfully submitted,

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Date: August 16, 2006

**Please recognize our Customer No. 20277
as our correspondence address.**

INFORMATION DISCLOSURE CITATION IN AN APPLICATION

ATTY. DOCKET NO.
061352-0119

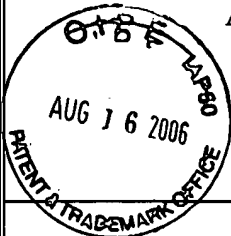
SERIAL NO.
10/564,085

APPLICANT
Tohru SAITOH, et al.

FILING DATE
January 11, 2006

GROUP
Not Yet Assigned

(PTO-1449)



U.S. PATENT DOCUMENTS

[illegible]

FOREIGN PATENT DOCUMENTS

[illegible]

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

EXAMINER'S INITIALS	CITE NO.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.
		Jagannathan, B., et al. "Self-Aligned SiGe NPN Transistors With 285 GHz f_{\max} and 207 GHz f_T in a Manufacturable Technology" IEEE Electron Device Letters, Vol. 23, No. 5, May 2002, pp. 258-260.

EXAMINER

DATE CONSIDERED

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

1 Applicant's unique citation designation number (optional). 2 Applicant is to place a check mark here if English language Translation is attached.